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(54) Abstract Title

Communication network

(57) A transmitter, for transmitting an intermittent sequence of messages to maintain synchronisation between the transmitter and at least one receiver, comprising: control means arranged to provide messages for transmission, each of said messages forming part of said sequence of messages and comprising control information for effecting synchronisation, including timing information, wherein said timing information is dependent upon when the transmission of a following message in the sequence occurs; and transmission means, responsive to said control means, for transmitting each of said messages. A receiver, for synchronising with a sequence of transmitted messages each comprising control information including timing information, comprising: receiver and synchronisation means responsive, when enabled, to the control information in a received message to indicate to the control means the reception of said message. The control means may enable power conservation by disabling said receiver and synchronisation means for a period of time dependent upon the timing information in said received message and enabling said receiver and synchronisation means to receive a following message in the sequence.

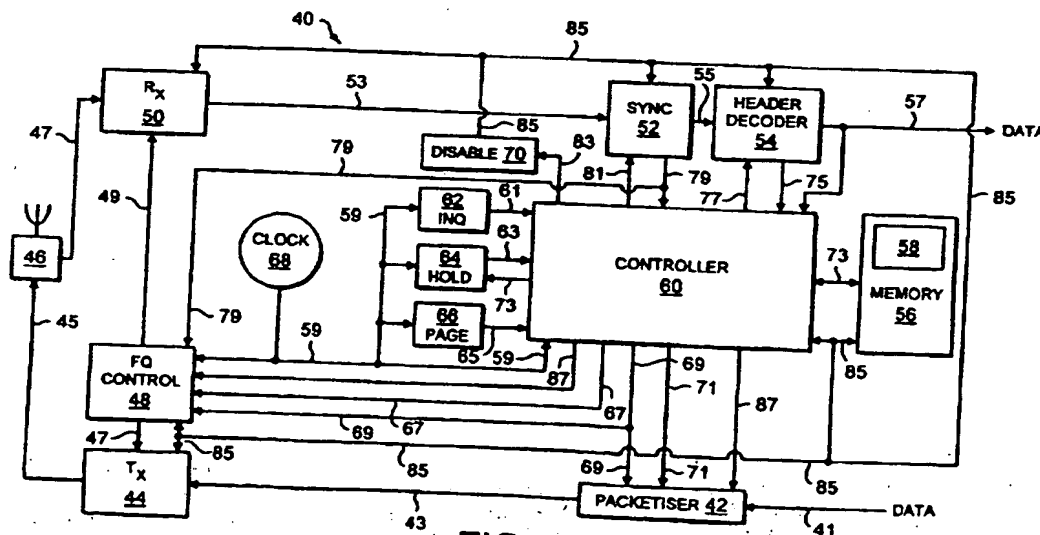


FIG. 4

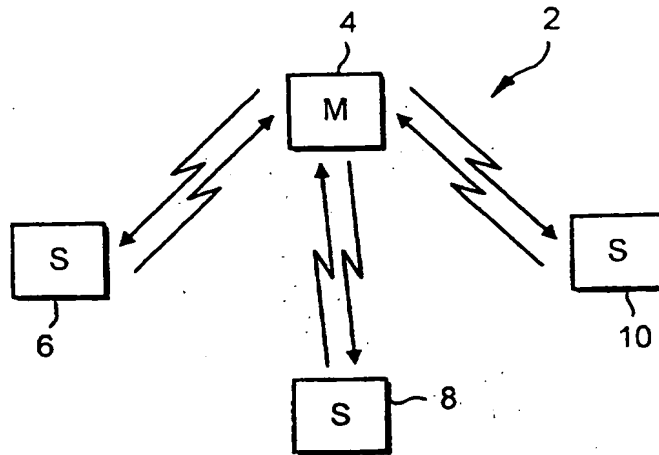


FIG. 1

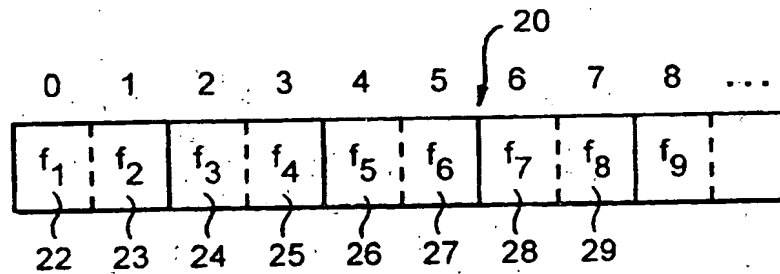


FIG. 2

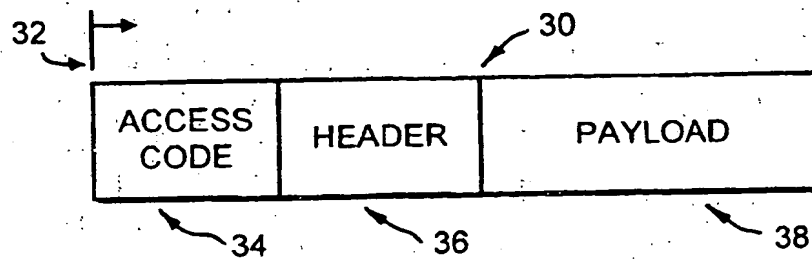


FIG. 3

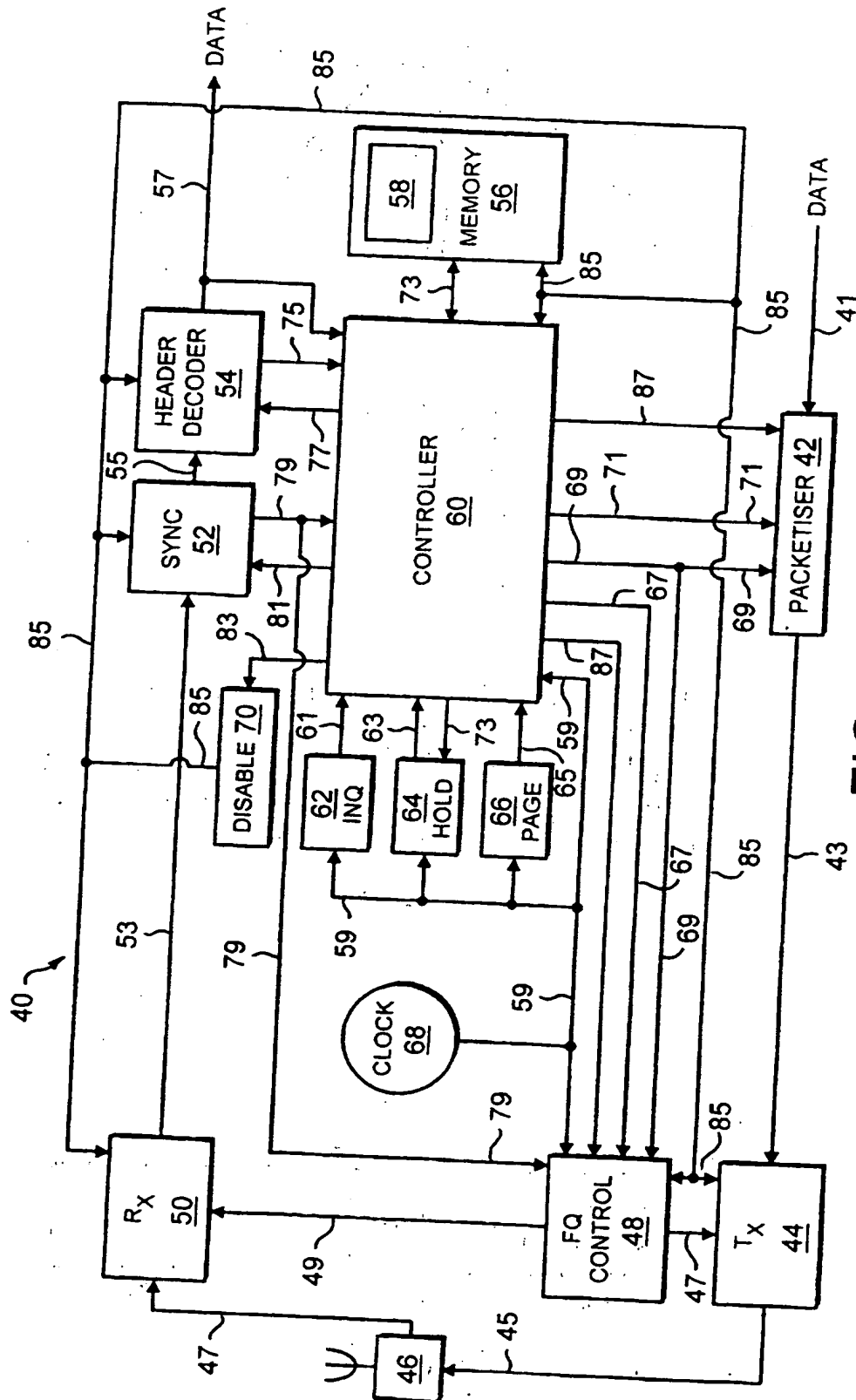


FIG. 4

FIG. 5

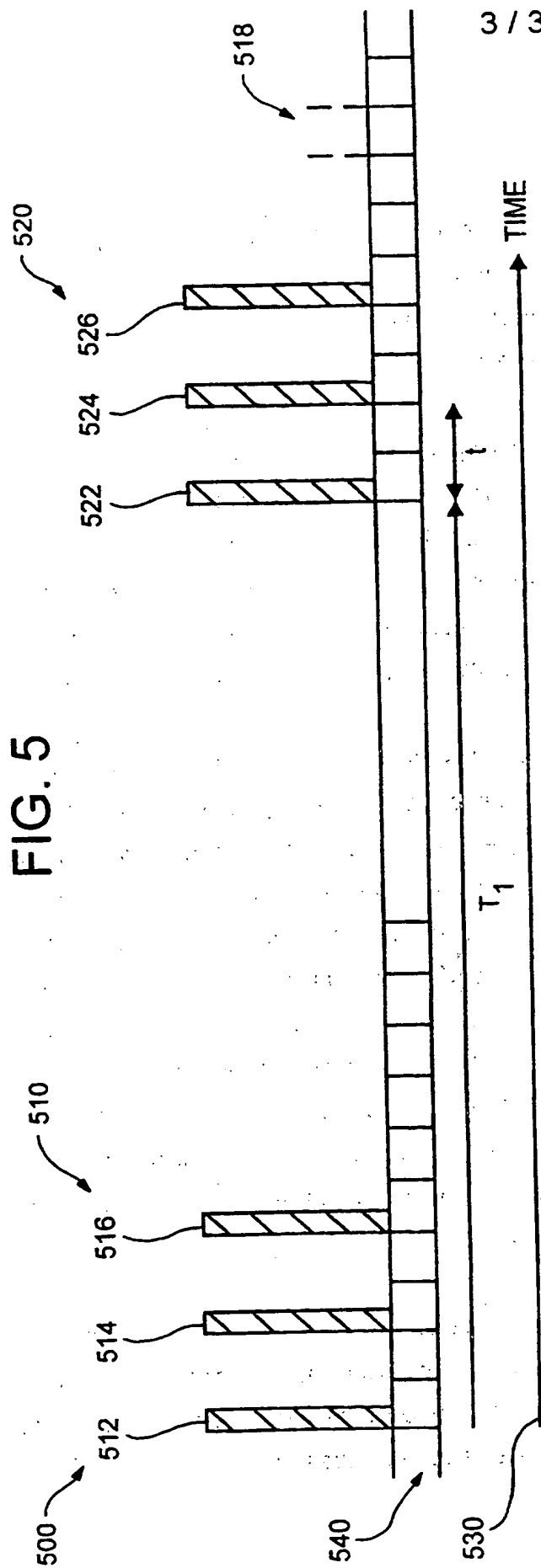
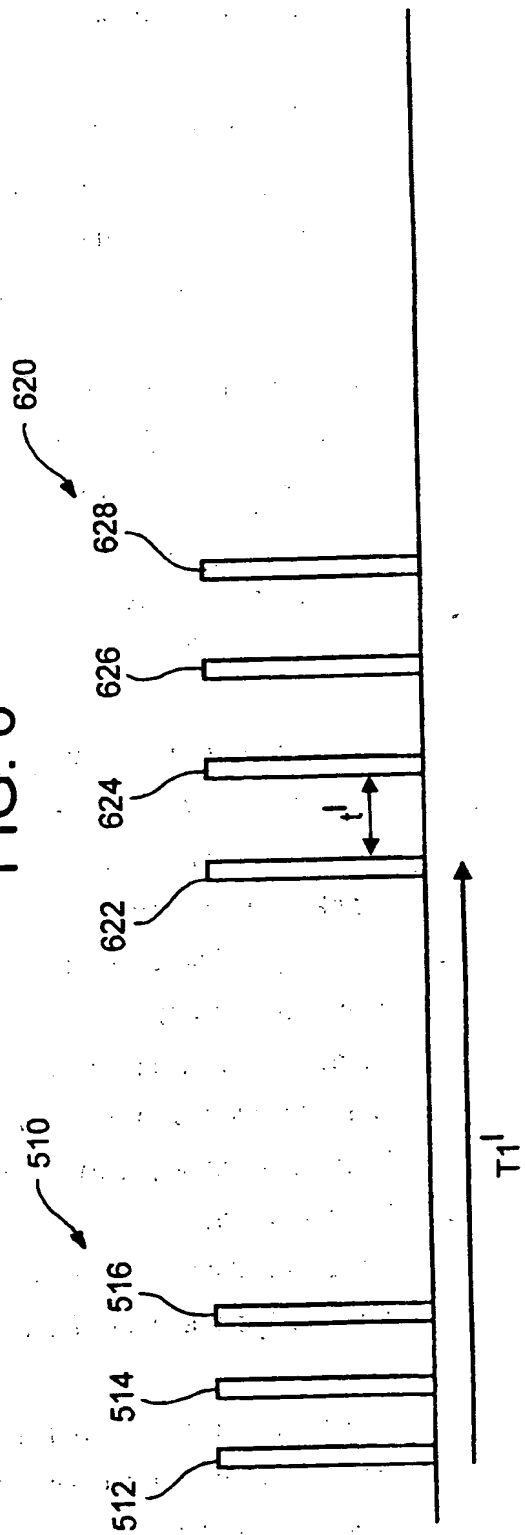


FIG. 6



Communication network

The present invention relates to the use of an intermittent sequence of
5 messages to maintain synchronisation between a transmitter and at least one
receiver in a communication network. In particular, it relates to transmitters
and receivers for use in such a network.

In a communication network comprising a plurality of transceivers it may be
10 necessary to keep the transceivers synchronised so that they use the same
timing for communicating between each other. One transceiver may act as a
master defining the timing for the communication system with the others
acting as slaves and keeping synchronised with the timing of the master. It
may be desirable to keep power consumption of the slaves as low as possible.

15 One way of maintaining synchronisation within the network is for the master to
transmit a periodic sequence of single beacon messages, with a fixed time
interval between the beacon messages. A beacon message may be a radio
packet. The slaves periodically listen, every fixed time interval, for a beacon
20 message. The periodic reception of a beacon message from the master allows
the slave to compare its timing with that of the master and to adjust its timing
to maintain synchronisation. Furthermore, as the sequence is periodic, power
consumption can be reduced if the slave receiver does not attempt to receive
beacon messages in the interval between beacon messages. Consequently,
25 the slave receivers may listen for a beacon message in a listening window of
fixed duration centred at the time a beacon message is expected to be
received, once every fixed time interval.

There are some problems with such a system. The system is susceptible to
30 the loss of beacon messages. Loss may arise from interference or multipath
propagation causing degradation of the beacon message such that it is not
received in the listening window of the receiver. Loss may also arise if the

timing of the master is varied. Such variation may be desirable when a transceiver is part of a host device such as a mobile phone which operates with a timing system which is not synchronised to the master. It may be useful to synchronise the host timing and the master timing by shifting the timing of the master so that it coincides with the host's timing.

It would be desirable to improve synchronisation within a communications network.

10 According to one aspect of the present invention there is provided transmitter, for transmitting an intermittent sequence of messages to maintain synchronisation between the transmitter and at least one receiver, comprising: control means arranged to provide messages for transmission, each of said messages forming part of said sequence of messages and comprising control
15 information for effecting synchronisation, including timing information, wherein said timing information is dependent upon when the transmission of a following message in the sequence occurs; and transmission means, responsive to said control means, for transmitting each of said messages.

20

According to another aspect of the present invention there is provided a receiver, for synchronising with a sequence of transmitted messages each comprising control information including timing information, comprising: control means arranged to control the operation of the receiver in dependence
25 on received ones of the transmitted messages; a clock for providing a time reference to the control means; and receiver and synchronisation means responsive, when enabled, to the control information in a received message to indicate to the control means the reception of said message,

30 wherein said control means is arranged to disable said receiver and synchronisation means for a period of time dependent upon the timing

information in said received message and to enable said receiver and synchronisation means to receive a following message in the sequence.

According to a further aspect of the present invention there is provided a receiver, for synchronising with a sequence of transmitted messages each comprising control information including timing information, comprising:
control means arranged to control the operation of the receiver in dependence on received ones of said transmitted messages;

a clock for providing a time reference to the control means; and
receiver and synchronisation means responsive to the control information in a received message to indicate to the control means the reception of said message,

wherein said control means is arranged to enable power conservation within the receiver for a period of time dependent upon the timing information in said received message, said period of time being such that power conservation is disabled to receive a following message in the sequence.

It will be appreciated that in embodiments of the invention, messages in the sequence of messages are used to indicate to the receiver when a following message in the sequence will arrive. Such indication may involve indicating a time period between messages, indicating the change to a time period between messages or indicating that no change to a time period between messages is required. Embodiments of the invention therefore provide flexible means of establishing and maintaining synchronisation.

For a better understanding of the present invention and to understand how the same may be carried into effect reference will now be made to the accompanying drawings in which:

Figure 1 illustrates a communications network including a master and slave units;

Figure 2 illustrates the time frame of the communications network;

Figure 3 illustrates a radio packet

Figure 4 illustrates a transceiver unit suitable for use as a master or slave; and
Figure 5 and 6 illustrate sequences of messages for synchronising transceiver
units in the network

5

Figure 1 illustrates a network 2 of radio transceiver units, including a master
unit 4 and slave units 6, 8 and 10, communicating by transmitting and
receiving radio packets. The master unit is the transceiver unit which initiates
the connection of a slave to the network. There is only one master in a
network. The network operates in a time division duplex fashion. The
transceiver units are synchronised to a common time frame determined by the
master unit 4. This time frame consists of a series of time slots of equal
length. Each radio packet transmitted in the network has its start aligned with
the start of a slot and a single packet is transmitted in the network at a time.

10

When the master unit is performing point-to-point communication a
transmitted radio packet is addressed to a particular transceiver which replies
to the master unit by transmitting a radio packet addressed to the master unit
in the next available time slot. When the master unit is performing point to
multi-point communication a transmitted radio packet is addressed to all
transceiver units. Any time misalignment between the master and a slave is
corrected by adjusting the timing of the slave.

20

The transceivers transmit and receive, in this example, in a microwave
frequency band, illustratively 2.4 GHz. The network reduces interference by
changing the frequency at which each radio packet is transmitted. A number
of separate frequency channels are assigned each with a bandwidth of 1MHz,
and the frequency may hop at a rate of 1600hops/s. The frequency hopping of
the transceivers communicating in or joining the network is synchronised and
controlled by the master unit. The sequence of hopping frequencies is unique
for the network and is determined by a unique identification of the master unit.
Each transceiver unit has a unique identification, the Unit ID, henceforth

30

referred to as the Slave ID for the slave units and the Master ID for a master unit.

The network is a radio frequency network suitable for transmitting voice information or data information between transceivers. The transmissions
 5 made are of low power, for example 0 to 20dBm, and the transceiver units can effectively communicate over the range of a few centimetres to a few tens or hundred of metres. The master unit has the burden of identifying the other transceiver units within its transmission range and the burden of paging a
 10 transceiver unit to set up a communication link between the master unit and that slave unit. Each of the slave units has a low power modes in which it neither transmits nor receives and other modes in which it receives and then responds to radio packets addressed to it by the master unit. A slave unit may remain in the low power mode except when transmitting or receiving.

15 Referring to Figure 2, a frame 20 is illustrated. This frame 20 is the common time frame used by the network 2 and controlled by the master unit 4. The frame illustratively has slots 22 to 29. The slots designated by even numbers are reserved. Only the master unit can begin transmitting a radio packet
 20 aligned with the start of the even numbered slots. The slots designated by odd numbers are reserved. Only radio packets transmitted by a slave, that is radio packets addressed for reception by the master unit can have their start aligned with the start of the odd numbered slots. Each slot is allocated a different one of a sequence of hopping frequencies. It is however, possible for
 25 a radio packet to extend over a number of slots and in this case the frequency at which the packet is transmitted remains constant at that allocated to the slot at the start of the packet. A slot has a constant time period and is typically 625 microseconds.

30 Referring to Figure 3, a typical radio packet 30 is illustrated. The radio packet has a start 32 and contains three distinct portions: a first portion contains an

Access Code 34, a second portion contains a Header 36 and a third portion contains a Payload 38.

The Access Code is a series of symbols used in the network to identify the start of a radio packet. It has a fixed length. The Access Code may comprise a Master ID, a Slave ID or an Inquiry Access Code. In a normal communication mode, the master and slave units use the Master ID as the Access Code. As there is only one master unit in a network, the Master ID identifies the network. When the master unit is in a Page Mode and is paging a particular slave unit to set up a communication link between itself and the slave unit, the Slave ID is used as the Access Code when the master addresses the slave and the slave replies. When the master unit is in an Inquiry Mode, the Inquiry Access Code is used as the Access Code when the master unit addresses the transceiver units and when a unit replies. The Inquiry Access Code identifies a packet as one to which all transceiver units must respond by transmitting their Slave ID.

The header 36 may or may not be present. If present, it has a fixed length. The header contains control words. The local address (L_ADDR) is a word uniquely identifying a slave within a network. The local address is assigned to a slave unit by the master unit when the master unit joins the slave to the network. The all zero L_ADDR is reserved for broadcast purposes. The packet identification word (PK_ID) specifies the features of the radio packet 30. PK_ID specifies whether a payload is present and its size and whether the payload contains data or transceiver control information.

The payload 38 carries either transceiver control information or voice/data information. The payload is of variable length and may be absent. When a slave unit receives a packet transmitted by a master unit in the Inquiry Mode it transmits a packet containing transceiver control information in its payload. This controlling radio packet has a payload contained at least two control words. The first is the Unit ID (SLAVE ID) of the slave unit and the second is

the value (SLAVE CLK) representing the local time kept by a clock in the slave unit. The controlling radio packet is used to allow the master to establish frequency and timing synchronisation with the slave. When a master unit is in the Page Mode it transmits a radio packet to the paged slave containing transceiver control information in its payload. This controlling radio packet has a payload containing at least two control words. The first is the Unit ID (MASTER ID) of the master unit and the second is a value (MASTER CLK) representing the local time kept by a clock in the master unit. The controlling radio packet is used by the slave to establish timing and frequency synchronisation with the master.

Referring to Figure 4, a schematic illustration of a transceiver unit is shown. Only as many functional blocks and interconnections are shown in this diagram as are necessary to explain in the following how a transceiver unit and the communication network operates. The transceiver unit 40 contains a number of functional elements including: an antenna 46, receiver 50, synchroniser 52, header decoder 54, controller 60, memory 56 having memory portion 58 storing the transceiver unit's Unit ID, disable circuitry 70, packetiser 42, clock 68, inquiry counter 62, hold counter 64, page scan counter 66, frequency hop controller 48 and transmitter 44. Although these elements are shown as separate elements they may in fact be integrated together and may be carried out in software or in hardware.

Data to be transmitted in the payload of a packet by the transceiver unit 40 is supplied as data signal 41 to the packetiser 42. Control information to be transmitted in the payload of a packet is supplied in a payload control signal 87 provided by the controller 60 to the packetiser 42. The packetiser 42 also receives an access code control signal 69 and a header control signal 71 from controller 60 which respectively control the Access Code 34 and the Header 36 attached to the payload to form the packet. The packetiser 42 places the data or control information into a packet 30 which is supplied as signal 43 to the transmitter 44. The transmitter 44 modulates a carrier wave in

dependence upon the signal 43 to produce the transmitted signal 45 supplied to the antenna 46 for transmission. The frequency of the carrier wave is controlled to be one of a sequence of hop frequencies by a transmission frequency control signal 47 supplied by the frequency hop controller 48 to the transmitter 44.

The antenna 46 receives a radio signal 51 and supplies it to the receiver 50 which demodulates the radio signal 51 under the control of a reception frequency control signal 49 supplied by the frequency controller 48 to produce a digital signal 53. The digital signal 53 is supplied to the synchroniser 52 which synchronises the transceiver unit 40 to the time frame of the network. The synchroniser is supplied with an access code signal 81 specifying the Access Code of the packet which the transceiver unit is expecting to receive. The synchroniser accepts those received radio packets with Access Codes which correspond to the expected Access Codes and rejects those received radio packets with Access Codes that do not correspond to the expected Access Code. A sliding correlation is used to identify the presence and the start of the expected Access Code in a radio packet. If the radio packet is accepted then the radio packet is supplied to the header decoder 54 as signal 55 and a confirmation signal 79 is returned to the controller 60 indicating that the packet has been accepted by the synchroniser 52. The confirmation signal 79 is used by the controller in a slave unit to resynchronise the slave clock to the master clock. The controller compares the time at which a radio packet was received with the time at which the radio packet was expected to be received and shifts its timing to offset the difference. Such an offset may be achieved by varying the value of M_OFFSET stored in memory 56 by the value of the difference. The header decoder 54 decodes the header in the received packet and supplies it to the controller 60 as header signal 75. The header decoder 54, when enabled by a payload acceptance signal 77 supplied by the controller 60, produces a data output signal 57 containing the remainder of the radio packet, the payload 38. The controller responds to a zero value of L_ADDR in the header signal 75 to enable the header decoder.

The data output signal 57 may contain transceiver control information. In this instance the data output signal 57 is supplied to controller 60 in response to the payload acceptance signal 77 provided by the controller 60.

5 The frequency-hopping controller 48 cycles through a sequence of frequencies. The transmission frequency control signal 47 and the reception frequency control signal 49 alternately control the transmitter 44 and the receiver 50. When the transceiver 40 is acting as a master, the receiver 50 is capable of receiving at frequencies determined by the odd values of the
10 sequence and the transmitter is capable of transmitting at frequencies determined by the even values of the sequence. When the transceiver is acting as a slave unit the reverse is true. The frequency-hopping controller 48 receives the access code control signal 69 (also supplied to the packetiser 42) and an offset signal 67 from the controller 60 and a clock signal 59, which
15 represents the time held in clock 68, from the clock 68. The offset signal 67 defines the value of an offset from the time held in the clock 68. This value may be null. The frequency-hopping controller combines the clock signal 59 and the offset signal 67 to emulate the time held in a clock offset by the value of the offset signal 67 from the clock 68. The sequence of frequencies through
20 which the hopping controller 48 cycles is dependent upon the access code control signal 69. The position within the cycle is dependent upon the emulated time. When the access code control signal 69 provides the value MASTER ID a master unit frequency-hopping sequence is defined. When the access code control signal 69 provides the value SLAVE ID a slave unit
25 frequency-hopping sequence is defined.

The clock 68 also supplies the clock signal 59 to the inquiry counter 62, the hold counter 64 and the page scan counter 66. Each of the counters contains values which are decremented with each clock cycle of the clock 68. The
30 counters supply a control signal to the controller 60 when the decremented value reaches a predetermined threshold. The Inquiry counter 62 produces an inquiry control signal 61 every few seconds. This signal causes the transceiver

unit 40 to enter the Inquiry Mode and the counter 62 to be reset. The hold counter 64 produces a hold disable signal 63 which causes the transceiver unit to exit a Hold Mode. The controller 60 initiates a Hold Mode by writing a value into the Hold counter 54 via write signal 73. The page scan counter 66
 5 produces a page scan control signal 65 every few seconds. This signal causes the transceiver unit to enter the Page Scan Mode and the counter 66 to be reset.

Disable circuitry 70 provides an enable signal 85 to the receiver 50, the
 10 synchroniser 52, the header decoder 54, the frequency-hopping controller 48, the transmitter 44 and the packetiser 42, the memory 56 and the controller 60 in the absence of which these elements would be switched off. The disable circuitry responds to an asserted disable control signal 83 supplied by the controller 60 to disassert the enable signal 85.

15

The memory 56 has a portion 58 which permanently stores the Unit ID of the transceiver unit 40 and the common Inquiry Access Code. The remaining portion of the memory 56 can be written to by the controller 60. If the transceiver unit 40 is functioning as a slave unit, the memory 56 will
 20 additionally store the Master ID, a value M_OFFSET representing the difference between the slave unit's clock and the master unit's clock and the slave's address in the network, L_ADDR. If the transceiver unit 40 is functioning as a master unit, the memory 56 will additionally store for each slave unit participating in the network: the Slave ID; a value S_OFFSET
 25 representing the difference between the master unit's clock and that particular slave unit's clock and L_ADDR uniquely identifying the particular slave unit in the network.

It should be noted that the access code signal 81, the access code control
 30 signal 69 and the offset signal 67 remain the same for adjacent duplex time slots, and that the access code signal 81 and the access code control signal 69 will have the same values.

The transceiver unit has various modes of operation including: the Standby Mode, the Communication Mode, the Inquiry Mode, the Page Mode, and the Hold Mode. The operation of the controller 40 in each of these modes may depend upon whether the transceiver unit is functioning as a master or as a slave.

Standby Mode

Before a transceiver unit 40 has been connected to a network it is in Standby Mode. To enter this Mode the controller 60 activates the disable control signal 83. The receiver 50, the transmitter 44, the synchroniser 52, the frequency-hopping controller 48, the header decoder 54, the packetiser 42, disable circuitry 70 and the controller 60 are disabled and do not draw power. Only the clock 68 and the counters 62, 64 and 66 are operational. In this mode power consumption is very low.

Inquiry Mode

Before a master unit can set up a communication network or join a transceiver unit to an existing network it needs to 'know' what transceiver units are within its transmission range. The master unit broadcasts inquiry radio packets in even numbered time slots. Each of the packets has the Inquiry Access Code as its Access Code and the zero L_ADDR in its header. If a slave unit is within range and is in an Inquiry Scan Mode it will respond in an odd numbered time slot, by transmitting a packet which has the Inquiry Access Code as its Access Code and has, in its payload, the SLAVE ID and SLAVE CLK of the slave unit.

Paging

Before a master unit can join a transceiver to the communication network it needs to give the slave unit some network parameters in the payload of the

paging radio packet. These parameters include: the MASTER ID so that the
 slave unit can recognise packets transmitted by the master unit and can
 emulate the master unit's frequency-hopping sequence; M_OFFSET so that
 the slave unit can keep in step with the master unit's time frame and
 5 frequency hopping; and L_ADDR so that the slave unit can recognise a
 packet addressed to it by the master unit. The master unit communicates
 these network parameters to the slave unit via a paging packet while
 emulating the slave unit's frequency-hopping sequence and keeping in step by
 emulating the slave's clock using SLAVE_ID as the value of access code
 10 control signal 69 and S_OFFSET as the value of offset signal 67. Once a
 communication link has been established between the master and slave they
 communicate using the master unit's frequency-hopping sequence. Each of
 the packets transmitted during paging has as its Access Code the SLAVE ID
 of the slave unit paged. The packets transmitted from master to slave unit are
 15 in even numbered time slots and the slave responds to the reception of these
 packets by transmitting packets in the associated duplex slot i.e. the
 immediately following odd numbered slot.

Communication Mode

20 In the communication mode, a slave can only transmit and must transmit in
 the slot immediately after that in which it was addressed. An exception to this
 is if the master unit is broadcasting, that is, transmitting to all slave units
 simultaneously. All packets transmitted in the communication mode have an
 25 Access Code determined by the MASTER ID and a frequency determined by
 the master unit's frequency-hopping sequence. The slave units emulate the
 master unit's frequency-hopping sequence using the stored value of MASTER
 ID as the access code control signal 69 and the stored value of M_OFFSET
 as the value of the offset signal 69 and synchronise their timing to the master
 30 unit using their own clock signal 59 and their stored value of M_OFFSET. The
 master addresses a particular slave unit by placing its unique address,
 L_ADDR, read from memory 56 in the header of a packet. The payloads of the

packets transmitted may contain data or control information such as updated network parameters.

Hold Mode

5

In the Hold Mode, no data is transmitted for a previously defined time interval. The slave unit in a Hold Mode maintains synchronisation to the master unit's time frame. In the Hold Mode a slave unit keeps the local address L__ADDR assigned by the master unit. The controller 60 initiates the Hold Mode by writing a value to the hold counter 64 via signal 73 and by enabling the disable circuitry 70. The controller deactivates the hold mode in response to the inquiry control signal 61, the hold disable signal 63 and the page scan control signal 65 respectively. The hold mode is initiated, for example, when in the communication mode, the master unit informs a slave unit that it will not receive a packet for a period of time SLEEP. The value of SLEEP is communicated to the slave unit in a payload and is supplied to the controller 60 via signal 57. The controller responds by writing the value SLEEP into the hold counter 64 and asserting the disable control signal 83. The slave unit thereby enters a low power mode, the hold mode. The hold mode may alternatively be activated by the controller 60 when the slave unit receives a radio packet addressed to another slave unit in the network. The controller via the header signal 75 and the value of PK_ID therein knows how long the network will be occupied by such a packet. The controller can enter the hold mode for a period at least as long as that equivalent to the time taken to transmit a packet having that size by writing an appropriate value into the hold counter 64. Additionally when the slave unit is not addressed in a particular slot in which the master unit can start transmitting, it will enter the hold mode for the remainder of that slot and the duration of the next slot.

30

Another mode similar to the Hold Mode is the Park Mode in which the slave unit's local address may be reallocated to another slave unit by the master unit. A slave is put in the Park Mode when it does not need to participate in

the network but needs to stay synchronised to the networks time frame. In the Park Mode a slave gives up its assigned L_ADDR and it can be reused by the master. In the Hold Mode the time at which the transceiver exits the Mode is predetermined, in the Park Mode it is not. The transceiver is woken up by the master.

Referring to Figure 5, there is illustrated a sequence 500 of messages 512, 514, 516, 522, 524 and 526. These messages are transmitted by the master unit and are used by the slave units to maintain time synchronisation with the master unit and are used to wake up the slave units from the Park Mode. The time arrow 530 illustrates that the messages are transmitted by the master in order from left to right, with message 512 being transmitted first. The common time frame 540 used by the units in the network illustrates that each of the messages is transmitted by the master in even slots only.

The sequence 500 of messages includes a first group of messages 510 and and a second group of messages 520. The first and second groups are separated by an interval of time T_1 . The first group 510 has a series of three messages: a first message 512, a second message 514, and a third message 516. The adjacent messages, that is, the first and second messages and the second and third messages are separated by the same time interval. The second group 520 of messages has a series of three messages: a first message 522, a second message 524 and a third message 526. The adjacent messages are separated by the same time interval, t .

A group of messages, for example, the second group 520, can be defined by its group parameters: the number of message packets per beacon group (N), the time interval between two adjacent message packets in the same group (t) and the time interval to the next beacon group (T_1). N represents the number of messages in the group. t represents the interval of time separating adjacent messages in the group. T_1 represents the interval of time separating the group in question from the group of messages immediately preceding it. A

slave transceiver with knowledge of these parameters can, after receiving one of the messages in the preceding group, accurately predict when the messages of the next group will be transmitted. Consequently if a message is expected by a slave and it is not received, the slave can try and receive the adjacent message and so on, until there are no more messages expected from that group. Thus using a group of messages instead of a single message reduces the probability of the slave missing an opportunity to synchronise with or be woken up by the master due to packet degradation, loss or time shifting.

Each message is a radio packet. Each of the messages is transmitted with a frequency synchronised with the master hopping frequency. The messages are transmitted in the master's transmission slots, that is, the even numbered slots. Each message is aligned with the beginning of a slot. Each radio packet message has MASTER ID as its access code, has the zero (broadcast) value of L_ADDR in its header, and contains a payload. The contents of the payload depend on the message packet type. If the message does not vary or update the group parameters the payload need not contain control information and may contain data. If the message does vary or update the group parameters the payload contains control information identifying the new parameters or the variations to the current parameters. The control information in the i th message in a group may take many forms but will at least be indicative, to the receiver, of the time interval between that message and the next beacon group and may indicate that value or the value of the variation to the previous value. All the messages in the sequence 500 comprise timing information as each message controls whether the group parameters t and $T1$ will or will not be updated and hence whether the timing of the sequence will or will not change.

According to the preferred embodiment the i th beacon message in a group contains the control information N , T_i and t . The value of N and t are the same for all the messages in a group. The values of T_i vary for each of the successive message packets in a group by t , as each message packet is t

closer to the next beacon group. Consequently referring to Figure 5, the first, second and third message packets 512, 514 and 516 have as their time interval to the next beacon group the value T_1 , $T_2=T_1-t$, and $T_3=T_1-2t$ respectively. It would be possible to provide instead of the absolute values (T_i) of the time interval to the next beacon group the value T_1 and an indication within the message packet whether it is the first, second or third message within the group.

Referring to Figure 6, the master may vary the group parameters between a first message group 510 and a second message group 620. The master informs the slave units of the variation via the message packets in the first message group. The first message group 510 is defined by the group parameters N , t and T_1 and the second group of messages is defined by the group parameters N' , t' and T_1' . Although the first group of messages is defined by the control information N , t and T_1 , each of the messages in the group comprises the control information defining the second group 620. For example, the first message 512 in the group contains the parameters N' , t' and T_1' , the second message in the group comprises the parameters N' , t' and $T_2'=T_1'-t'$ etc. The second group 620 is defined by the new group parameters N' , t' , and T_1' . Each message in the second group of parameters comprises the control information defining the third group of messages (not shown).

The second group 620 has a series of four messages 622, 624, 626 and 628 with adjacent messages separated by an interval t' . In this example the master has shifted the timing frame of the communication network by varying the time interval between a first message group 510 and the second following message group 620 by a value S , where S equals the difference between T_1 and T_1' . The shift S in synchronous systems is an integer number of time slots. The master may continue to transmit the first group of messages 510 with a periodicity T_1 , for a short while to ensure that any slave which may have inadvertently missed the first transmission of the first group does not lose synchronisation. If the shift value S is small and particularly if it is an

integer multiple of t not exceeding Nt then no retransmission of the first group is required as there is some overlap between the messages of the second group and where the messages of the second group would be expected if no variation in the group parameters occurred. Although in this example, all the group parameters were varied it should be appreciated that any one or any combination of the group parameters may be varied. It is therefore possible to vary the parameters of the park mode and in particular to shift the time reference of the communication network without waking up and separately transmitting to the slaves.

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Referring now to Figure 4, the operation of a transceiver operating as a master unit to create the sequence of messages 500 will be explained. For the purposes of this explanation consider the master to have previously transmitted a first group of messages and to be about to transmit a second group of messages. The controller stores the group parameters N , $T1$ and defining the second group of messages in the memory 56. These parameters were transmitted in the first message of the first group of messages. The controller, when a period of time $T1$ has expired since the transmission of the first message in the first group of messages, initiates the transmission of the second group of messages. The controller controls the frequency at which transmission occurs via the access code control signal 69, the offset signal 67 and the clock signal 68. The controller provides the value of the MASTER ID read from the memory portion 58 as the access code control signal 67 and a null value as the offset signal 67. The controller provides the zero value of L_ADDR in the header control signal 71. The controller then determines which group parameters N' , t' and $T1'$ should define a third group of messages to be transmitted a period of time $T1'$ after the transmission of the first message of the second group of messages. The controller places the values of the group parameters N' , t' and $T1'$ in the payload of a first packet of the second group as control information via packet control signal 87 and writes the parameters to memory 56. The first packet is transmitted by transmitter 44. The controller waits a period of time t and then places the values of the parameters N' , t' and

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- $T2'=T1'-t$ in the payload of a second packet as control information via packet control signal 87. The second packet is transmitted by the transmitter 44. The controller waits a period of time t and then places the values of the group parameters N' , t' and $T3'=T1'-2.t$ in the payload of a third packet as control information via packet control signal 87. The third packet is transmitted by the transmitter 44. This process continues until the N packets of the second group have been transmitted. The controller then waits a period of time $T1'-N.t$ before initiating the transmission of the next group of messages.
- 10 Referring now to Figure 4, the operation of a transceiver operating as a slave unit synchronising with the sequence of messages 500 will be explained. In the park mode all the elements of the transceiver except the clock 68 and the counters 62, 64 and 66 are normally disabled by disable circuitry 70. When the hold counter 64 expires, the controller 60 disables the disable circuitry 70
- 15 for a predetermined period of time. The transceiver is able to receive transmitted messages during the reception window defined by the predetermined period of time. The shorter the reception window the lower the power consumption in the transceiver. The controller reads the value of MASTER ID and M_OFFSET from the memory 56 and supplies them as
- 20 access code control signal 69 and offset signal 67 respectively to the frequency hopping controller 48. The frequency hopping controller causes the receiver 50 to receive at the same frequencies at which the master is transmitting. For the purposes of this example it is to be assumed that the receiver previously received the i th message in a first group of messages.
- 25 This message contained a payload comprising as control information the parameters t , N and T_i defining the second group of messages. These parameters have been stored in memory 56 and the value of T_i written to the hold counter 64. The controller reads from memory 56 the group parameters (N , T_i and t) of the second group of messages which is about to be transmitted
- 30 to the receiver. The reception window controlled by the controller may be centred at a time T_i after the transmission of the i th message in the first group. Consequently the receiver 50 is synchronised in time via signal 85 and

synchronised in frequency via the reception frequency control signal 49 with the transmission of the second group of messages from the master.

The controller provides the value of MASTER ID to the synchroniser 52 via access code signal 81. If a packet having the correct access code is received
 5 in the reception window the controller is informed by confirmation signal 79. The controller compares the time at which the packet was received as indicated by the confirmation signal 79 with the time at which a packet was expected to be indicated as received by the confirmation signal and corrects
 10 M_OFFSET stored in memory 56 by the difference. Such a correction ensures any small drifts between the timing of the master and the timing of the slave are corrected each time a group of messages is transmitted by the master. The header decoder accepts the packet as it was broadcasted (L_ADDR zero) and passes the contents of the packet's payload to the controller via data
 15 output signal 57. The controller extracts the values of the third group's parameters from the payload and stores them in the memory 58. The receiver should receive the first message in the payload and the parameters stored to memory should be N' , t' and $T1'$, however, the first message may be missed and the i th message received (see below) and in this case the parameters
 20 stored in memory are N' , t' and Ti' . The controller then writes the value of Ti' to the hold counter 64 and activates disable circuitry 70 via disable control signal 83.

If a packet is not received in the reception window the controller activates
 25 disable circuitry 70 for a period of time t and then disables the circuitry for a predetermined duration thereby opening a second reception window. If a packet is not received in the second reception window, the controller activates disable circuitry for a period of time t and then disables the circuitry for a predetermined duration thereby opening a third reception window. This
 30 process continues until the N th reception window has been unsuccessfully opened. The controller then writes the value TN into the hold counter 64.

Each group of messages within the sequence can perform a different function. All the messages have at least the access code of the master so that the synchroniser 52 can function correctly. The messages in a group allow a slave to synchronise with very little scan activity. The messages in a group may contain the group parameters which allow the slave to remain synchronised to the sequence. Such messages may change the group parameters or keep them the same. Alternatively, the messages within a group can act as wake up messages allowing the master to page them directly in a subsequent time slot. The payload of such a message contains three value. The value T_i indicates the time until the transmission of the next group in the sequence. The SLAVE ID of the slave unit to be paged identifies the payload as addressed to a particular slave. The newly assigned L_ADDR for the slave allows a subsequent page message to be addressed to the appropriate slave. Alternatively, the messages in a group can contain data in their payloads and not carry any group parameters. These messages however indicate to a slave that the group parameters should not change.

Transceivers such as that illustrated in Figure 4, may form part of different devices such as mobile phones, computers, pagers, a computer mouse, headsets, microphones etc. Such transceivers allow the devices to form a communication network and exchange data or control information via the network.

The preceding description describes a preferred implementation of the claimed invention in a preferred application, namely a low power radio frequency communications network. However, it should be appreciated that other implementations and applications may be utilised without departing from the scope of the invention as claimed.

Claims

1. A transmitter, for transmitting an intermittent sequence of messages to maintain synchronisation between the transmitter and at least one receiver,
5 comprising:
control means arranged to provide messages for transmission, each of said messages forming part of said sequence of messages and comprising control information for effecting synchronisation, including timing information, wherein said timing information is dependent upon when the transmission of a
10 following message in the sequence occurs; and
transmission means, responsive to said control means, for transmitting each of said messages.
2. A receiver, for synchronising with a sequence of transmitted messages
15 each comprising control information including timing information, comprising:
control means arranged to control the operation of the receiver in dependence on received ones of the transmitted messages;
a clock for providing a time reference to the control means; and
receiver and synchronisation means responsive, when enabled, to the control
20 information in a received message to indicate to the control means the reception of said message,
wherein said control means is arranged to disable said receiver and
synchronisation means for a period of time dependent upon the timing
information in said received message and to enable said receiver and
25 synchronisation means to receive a following message in the sequence.
3. A receiver, for synchronising with a sequence of transmitted messages
each comprising control information including timing information, comprising:
control means arranged to control the operation of the receiver in dependence
30 on received ones of said transmitted messages;
a clock for providing a time reference to the control means; and

receiver and synchronisation means responsive to the control information in a received message to indicate to the control means the reception of said message,

5 wherein said control means is arranged to enable power conservation within the receiver for a period of time dependent upon the timing information in said received message, said period of time being such that power conservation is disabled to receive a following message in the sequence.

4. A transmitter as claimed in claim 1 wherein said timing information is
10 indicative, to a receiver, of the period of time between the transmission of a message containing that timing information and the transmission of a following message in the sequence.

5. A transmitter as claimed in claim 1 or 4 or a receiver as claimed in
15 claim 2 or 3 wherein said timing information comprises the value of the period of time between the transmission of a message containing that timing information and the transmission of said following message in the sequence.

6. A transmitter as claimed in claim 1 or 4 or a receiver as claimed in
20 claim 2 or 3 wherein said timing information comprises the value of the period of time between the transmission of a message containing that timing information and the transmission of the following message in the sequence compared to the period of time between the transmission of the message containing that timing information and the transmission of a preceding
25 message in the sequence.

7. A transmitter or receiver as claimed any preceding claim wherein said following message is the next message in the sequence.

8. A transmitter or receiver as claimed in claim 7, when dependent upon
30 claim 6 wherein said preceding message is the directly preceding message.

9. A transmitter or receiver as claimed in any one of claims 1 to 6, wherein said sequence of messages comprises a sequence of groups of messages each of said groups of messages comprising a plurality of messages in series.
- 5 10. A transmitter or receiver as claimed in claim 9 wherein said following message is a message in a following group.
11. A transmitter or receiver as claimed in claim 10 wherein said following group is the next group.
- 10 12. A transmitter or receiver as claimed in claim 9, 10 or 11, when dependent upon claim 6, wherein said preceding message is a message in a preceding group.
- 15 13. A transmitter or receiver as claimed in claim 12 wherein said preceding group is the directly preceding group.
14. A transmitter or receiver as claimed in any preceding claim wherein said control information identifies the receiver.
- 20 15. A transmitter or receiver as claimed in any preceding claim wherein said control information identifies the messages as broadcasted messages.
16. A transmitter or receiver as claimed in any preceding claim wherein said control information comprises a sequence for correlation by the receiver.
- 25 17. A transmitter or receiver as claimed in claim 10 wherein said control information comprises a value identifying the number of messages in the following group

18. A transmitter or receiver as claimed 10 or 17, wherein said control information comprises a value identifying the time between messages in the following group.

5 19. A transmitter as claimed in claim 11 wherein said control means in arranged to vary the time between the transmission of a pair of successive groups of messages by an amount such that there is coincidence between the time of transmission of a message in the following group of the pair and the expected time of transmission, in the absence of a variation, of a message in
10 the following group

20. A transmitter as claimed in claim 19 wherein for said pair of successive groups, said series of messages within each of said pair of groups are separated by equal time intervals.

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21. A receiver as claimed in any preceding claim wherein said control means enables said receiver and synchronisation means, to receive a following packet in the sequence, for a predetermined duration.

20 22. A receiver as claimed in claim 21 wherein said control means in the absence of an indication from the receiving and synchronising means that a message has been received, re-enables said receiver and synchronisation means, to receive said following packet.

25 23. A receiver as claimed in claim 21 when dependent upon claim 16 wherein said re-enablement is controlled for reception of the messages in the following group.

30 24. A receiver as claimed in claim 23 wherein said control means re-enablement is intermittent, the interval between enablement depending upon the time between messages in the group.

25. A receiver as claimed in any preceding claim wherein said control means is arranged to compare the expected time of arrival of a message, provided by the timing information in a preceding received message, with the actual time of arrival of the message as indicated to the control means by the reception and synchronisation means, and to offset said time reference provided by the clock in dependence on said comparison.
26. A transceiver comprising a receiver and/or a transmitter as claimed in any preceding claim.
27. A computer comprising a receiver and/or a transmitter as claimed in any one of claims 1 to 25.
28. A mobile telephone comprising a receiver and/or a transmitter as claimed in any one of claims 1 to 25.
29. An accessory for a mobile telephone comprising a receiver and/or a transmitter as claimed in any one of claims 1 to 25.
30. A communication network comprising transceivers as claimed in claim 26.
31. A transceiver substantially as hereinbefore described with reference to the accompanying figures and/or as shown in the figures.



Application No: GB 9820861.4
Claims searched: 1 to 31

Examiner: Ken Long
Date of search: 19 April 1999

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Patents Act 1977 Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK CI (Ed.Q): H4P (PSB) H4L (LECTP) G4H (HRCA)

Int CI (Ed.6): H04L (7/00) H04Q (7/32 & 7/18) G08B (3/10)
H04M (1/72) H04B (1/16)

Other: ONLINE : EPODOC, WPI, JAPIO

Documents considered to be relevant:

Category	Identity of document and relevant passage		Relevant to claims
X	GB 2115195 A	GENERAL ELECTRIC (page 1 lines 5-23, page 2 lines 49-61 and page 3 lines 1-25)	1-9, 14, 16, 21, 26, 28 & 30
X	EP 0711089 A2	MOTOROLA (column 1 lines 5-10 and column 5 lines 37-58)	1-9, 14, 16, 21, 26, 28 & 30
X	WO 95/27273 A1	BELL (page 2 line 28 to page 3 line 9 and page 9 line 22)	1-9, 14, 16, 21, 26, 28 & 30
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